

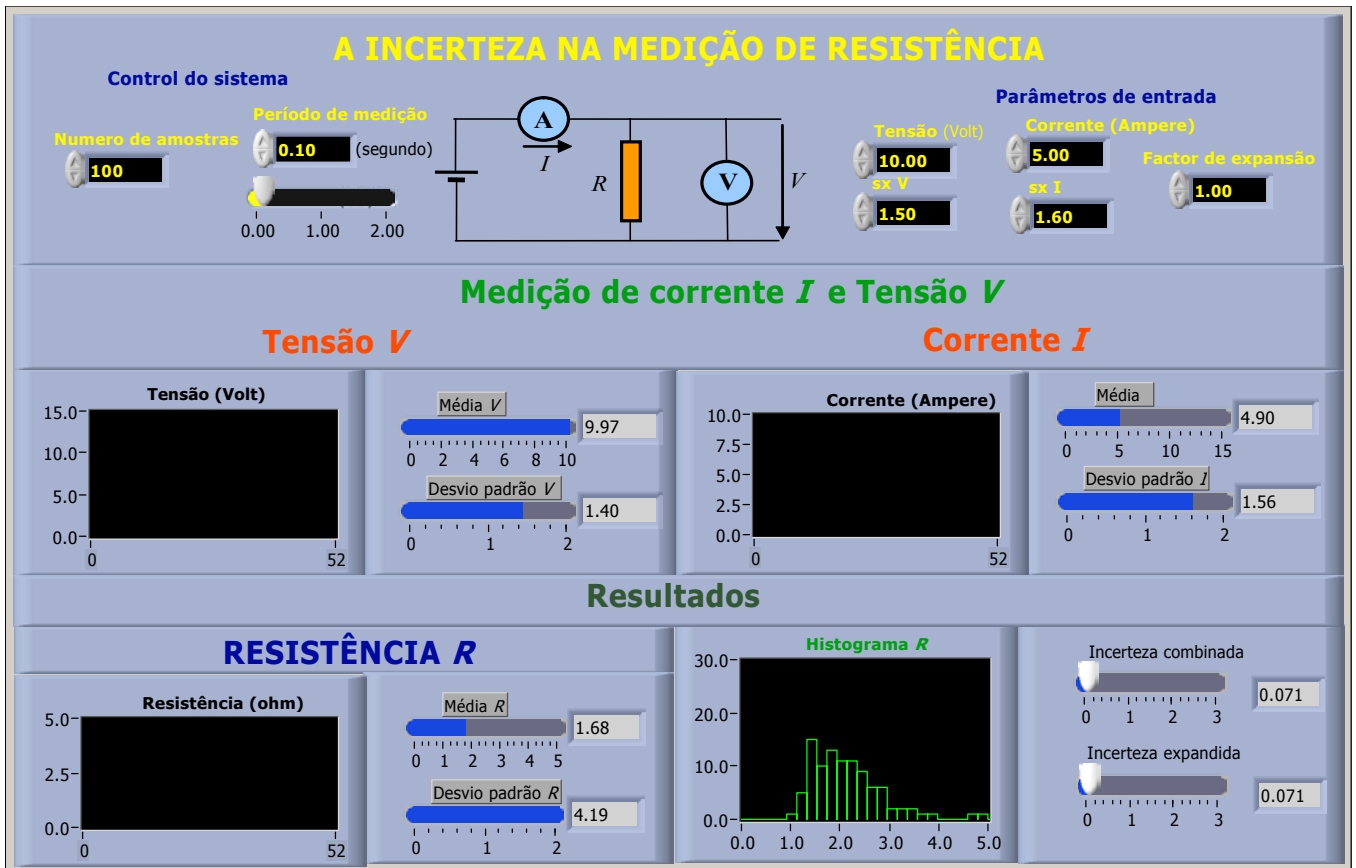
Connector Pane



IM-incerteza.vi

This VI simulates the measure of resistance computes a statistical mean, standard deviation, and histogram of voltage, current and resistance.

Front Panel



Block Diagram

